

Notice of Allowability

Application No.

10/731,442

Applicant(s)

KOYAMA, JUN

Examiner

Nitin Patel

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-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address--

All claims being allowable, PROSECUTION ON THE MERITS IS (OR REMAINS) CLOSED in this application. If not included herewith (or previously mailed), a Notice of Allowance (PTOL-85) or other appropriate communication will be mailed in due course. **THIS NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT RIGHTS.** This application is subject to withdrawal from issue at the initiative of the Office or upon petition by the applicant. See 37 CFR 1.313 and MPEP 1308.

1. ☒ This communication is responsive to 01/18/2007.
2. ☒ The allowed claim(s) is/are 1-44.
3. ☒ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
- a) ☒ All b) ☐ Some* c) ☐ None of the:
1. ☒ Certified copies of the priority documents have been received.
 2. ☐ Certified copies of the priority documents have been received in Application No. _____.
 3. ☐ Copies of the certified copies of the priority documents have been received in this national stage application from the International Bureau (PCT Rule 17.2(a)).

* Certified copies not received: _____.

Applicant has THREE MONTHS FROM THE "MAILING DATE" of this communication to file a reply complying with the requirements noted below. Failure to timely comply will result in ABANDONMENT of this application.

THIS THREE-MONTH PERIOD IS NOT EXTENDABLE.

4. ☐ A SUBSTITUTE OATH OR DECLARATION must be submitted. Note the attached EXAMINER'S AMENDMENT or NOTICE OF INFORMAL PATENT APPLICATION (PTO-152) which gives reason(s) why the oath or declaration is deficient.
5. ☐ CORRECTED DRAWINGS (as "replacement sheets") must be submitted.
- (a) ☐ including changes required by the Notice of Draftsperson's Patent Drawing Review (PTO-948) attached
- 1) ☐ hereto or 2) ☐ to Paper No./Mail Date _____.
- (b) ☐ including changes required by the attached Examiner's Amendment / Comment or in the Office action of Paper No./Mail Date _____.
- Identifying indicia such as the application number (see 37 CFR 1.84(c)) should be written on the drawings in the front (not the back) of each sheet. Replacement sheet(s) should be labeled as such in the header according to 37 CFR 1.121(d).
6. ☐ DEPOSIT OF and/or INFORMATION about the deposit of BIOLOGICAL MATERIAL must be submitted. Note the attached Examiner's comment regarding REQUIREMENT FOR THE DEPOSIT OF BIOLOGICAL MATERIAL.

Attachment(s)

1. ☐ Notice of References Cited (PTO-892)
2. ☐ Notice of Draftsperson's Patent Drawing Review (PTO-948)
3. ☒ Information Disclosure Statements (PTO/SB/08),
Paper No./Mail Date 01/18/2007
4. ☐ Examiner's Comment Regarding Requirement for Deposit
of Biological Material
5. ☐ Notice of Informal Patent Application
6. ☐ Interview Summary (PTO-413),
Paper No./Mail Date _____
7. ☐ Examiner's Amendment/Comment
8. ☒ Examiner's Statement of Reasons for Allowance
9. ☐ Other _____


NITIN I. PATEL
PRIMARY EXAMINER

Continued Examination Under 37 CFR 1.114

1. A request for continued examination under 37 CFR 1.114, including the fee set forth in 37 CFR 1.17(e), was filed in this application after allowance or after an Office action under *Ex Parte Quayle*, 25 USPQ 74, 453 O.G. 213 (Comm'r Pat. 1935). Since this application is eligible for continued examination under 37 CFR 1.114, and the fee set forth in 37 CFR 1.17(e) has been timely paid, prosecution in this application has been reopened pursuant to 37 CFR 1.114. Applicant's submission filed on 01/18/2007 has been entered.

REASON FOR ALLOWANCE

2. Claims 1-44 is allowed.

3. The following is an examiner's statement of reason for allowance:

The prior art fails to teach or suggest a display device comprising: a plurality of pixels each having a switching element and a light-emitting a substrate in matrix, a plurality of source signal lines, one gate signal line and a source signal line driver circuit having a constant current source, wherein the constant current source is electrically connected to one of a plurality of source signal lines for one pixel column; and one gate signal line for one pixel row, wherein the switching element has an input terminal, an output terminal, and a control terminal; wherein the input terminal is electrically connected to any one of the plurality of source signal lines; wherein the output terminal is electrically connected to the light-emitting element; and wherein the control terminal is electrically connected to the gate signal line as claimed in claim 1.

The prior art fails to teach or suggest a display device comprising: a plurality of

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pixels each having a switching element and a light-emitting element over a substrate in matrix; a plurality of source signal lines for one pixel column; one gate signal line for one pixel row; and a plurality of source signal line driver circuits each electrically connected to at least one of the plurality of source signal lines, wherein the switching element has an input terminal, an output terminal, and a control terminal; wherein the input terminal is electrically connected to any one of the plurality of source signal lines; wherein the output terminal is electrically connected to the light-emitting element; and wherein the control terminal is electrically connected to the gate signal line as claimed in claim 2.

The prior art fails to teach or suggest a display device comprising: a plurality of pixels each having a switching element and a light-emitting element on over a substrate in matrix; a plurality of source signal lines for one pixel column; and gate signal lines each for one pixel row, and a gate signal line driver circuit connected to the gate signal line and a source signal line driver having a constant current source, wherein the constant current source is electrically connected to one of the plurality of source lines, wherein the switching element has an input terminal, an output terminal, and a control terminal; wherein the input terminal is electrically connected to any one of the plurality of source signal lines; wherein the output terminal is electrically connected to the light-emitting element; and wherein the control terminal is electrically connected to one of the gate signal lines as claimed in claim 3.

The prior art fails to teach or suggest a driving method of a display device comprising the steps of: turning switching elements ON by driving a plurality of gate signal lines simultaneously; inputting a signal of one of a plurality of source signal lines

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to a light-emitting element; and driving the light-emitting element. wherein a plurality of pixels each having a switching element and the light-emitting element is disposed on a substrate in matrix; wherein the plurality of source signal lines is disposed for one pixel column; wherein each of the plurality of gate signal lines is disposed for one pixel row; wherein the switching element has an input terminal, an output terminal, and a control terminal; wherein the input terminal is electrically connected to one of the plurality of source signal lines; wherein the output terminal is electrically connected to the light-emitting element; and wherein the control terminal is electrically connected to one of the plurality of the gate signal lines as claimed in claim 19.

4. Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably accompany the issue fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance."

Conclusion

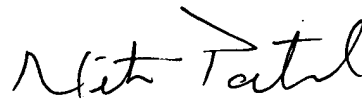
5. Any inquiry concerning this communication or earlier communications from the examiner should be directed to Nitin Patel whose telephone number is 571-272-7677.

The examiner can normally be reached on 8:00-5:00.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Bipin H. Shalwala can be reached on 571-272-7681. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

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Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free). If you would like assistance from a USPTO Customer Service Representative or access to the automated information system, call 800-786-9199 (IN USA OR CANADA) or 571-272-1000.

A handwritten signature in black ink, appearing to read "Nitin Patel", with a stylized flourish at the end.

NITIN I. PATEL
PRIMARY EXAMINER